Comparison of SVC and STATCOM in Static Voltage Stability Margin Enhancement

Mehrdad Ahmadi Kamarposhti and Mostafa Alinezhad

Abstract—One of the major causes of voltage instability is the reactive power limit of the system. Improving the system's reactive power handling capacity via Flexible AC transmission System (FACTS) devices is a remedy for prevention of voltage instability and hence voltage collapse. In this paper, the effects of SVC and STATCOM in Static Voltage Stability Margin Enhancement will be studied. AC and DC representations of SVC and STATCOM are used in the continuation power flow process in static voltage stability study. The IEEE-14 bus system is simulated to test the increasing loadability. It is found that these controllers significantly increase the loadability margin of power systems.

Keywords—SVC, STATCOM, Voltage Collapse, Maximum Loading Point.

I. INTRODUCTION

TN recent years, the increase in peak load demand and power Itransfers between utilities has elevated concerns about system voltage security. Voltage collapse has been deemed responsible for several major disturbances and significant research efforts are under way in an effort to further understand voltage phenomena. A large portion of this research is concentrated on the steady state aspects of voltage stability. Indeed, numerous authors have proposed voltage stability indexes based upon some type of power flow analysis. A particular dsculty being encountered in such research is that the Jacobian of a Newton-Raphson power flow becomes singular at the steady state voltage stability limit. In fact, this stability limit, also called the critical point, is often defined as the point where the power flow Jacobian is singular. As a consequence, attempts at power flow solutions near the critical point are prone to divergence and error. For this reason, double precision computation and anti divergence algorithms have been used in attempts to overcome the numerical instability [1].

Voltage instability is mainly associated with reactive power imbalance. The loadability of a bus in the power system depends on the reactive power support that the bus can receive from the system. As the system approaches the Maximum Loading Point (MLP) or voltage collapse point, both real and reactive power losses increase rapidly. Therefore, the reactive

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power supports have to be local and adequate.

There are two types of voltage stability based on the time frame of simulation: static voltage stability and dynamic voltage stability. Static analysis involves only the solution of algebraic equations and therefore is computationally less extensive than dynamic analysis. Static voltage stability is ideal for the bulk of studies in which voltage stability limit for many pre-contingency and post-contingency cases must be determined.

In static voltage stability, slowly developing changes in the power system occur that eventually lead to a shortage of reactive power and declining voltage. This phenomenon can be seen from the plot of the power transferred versus the voltage at receiving end. The plots are popularly referred to as P-V curve or "Nose" curve. As the power transfer increases, the voltage at the receiving end decreases. Eventually, the critical (nose) point, the point at which the system reactive power is short in supply, is reached where any further increase in active power transfer will lead to very rapid decrease in voltage magnitude. Before reaching the critical point, the large voltage drop due to heavy reactive power losses can be observed.

The only way to save the system from voltage collapse is to reduce the reactive power load or add additional reactive power prior to reaching the point of voltage collapse [2].

Voltage collapse phenomena in power systems have become one of the important concerns in the power industry over the last two decades, as this has been the major reason for several major blackouts that have occurred throughout the world including the recent Northeast Power outage in North America in August 2003 [3]. Point of collapse method and continuation method are used for voltage collapse studies [4]. Of these two techniques continuation power flow method is used for voltage analysis. These techniques involve the identification of the system equilibrium points or voltage collapse points where the related power flow Jacobian becomes singular [5, 6].

Usually, placing adequate reactive power support at the "weakest bus" enhances static-voltage stability margins. The weakest bus is defined as the bus, which is nearest to experiencing a voltage collapse. Equivalently, the weakest bus is one that has a large ratio of differential change in voltage to differential change in load $(\partial V/\partial P_{Total})$. Changes in voltage at each bus for a given change in system load is available from the tangent vector, which can be readily obtained from the

predictor steps in the CPF process. In addition to the above method, the weakest bus could be obtained by looking at right eigenvectors associated with the smallest eigenvalue as well.

Reactive power support can be done with FACTS devices. Each FACTS device has different characteristics; some of them may be problematic as far as the static voltage stability is concerned. Therefore, it is important to study their behaviors in order to use them effectively.

Canizares and Faur studied the effects of SVC and TCSC on voltage collapse [7]. Study of STATCOM and UPFC Controllers for Voltage Stability Evaluated by Saddle-Node Bifurcation Analysis is carry out in [8].

In this paper is to compare the merits and demerits of two FACTS devices, namely, SVC and STATCOM in terms of Maximum Loading Point (MLP) in static voltage collapse study.

Rest of the paper is organized as follows: Section II briefly introduces the basic mathematical tools required for the analysis of voltage collapse phenomena. A brief introduction of the stability models including AC and DC representations of SVC and STATCOM is presented in Section III. Section IV is depicted to simulation of voltage collapse phenomena on IEEE 14 bus test system with implementing SVC and STATCOM. Section V reviews the main points discussed in this paper.

II. VOLTAGE COLLAPSE

Voltage collapse studies and their related tools are typically based on the following general mathemathical descriptions of the system [9]:

$$x = f(x, y, \lambda, p)$$

$$0 = g(x, y, \lambda, p)$$
(1)

Where $x \in \Re^n$ represents the system state variables, corrosponding to dynamical states of generators, loads, and any other time varying element in the system such as FACTS devices; $y \in \Re^n$ corresponds to the algebraic variables, usually associated to the transmission system and steady state element models, such as some generators and loads in the network; $\lambda \in \Re^n$ stands for a set of uncontrolled parameters that drive the system to voltage collapse, which are typically used to represent system demand. Vector $p \in \Re^n$ is used here to represent system parameters that are directly controllable, such as shunt and series compensation levels.

Based on equation (1) the voltaage collapse point may be defined, under certain assumptions, as the equblirium point where the related system jacobian is singular, i.e. the point $(x_0, y_0, \lambda_0, p_0)$ where and $D_x F|_0$ has a zero eigenvalue. This equblirium is typically associated to a saddle-node bifurcation point.

For a given set of controllable parameters P_0 , voltage collapse studies usually concentrate on determining the collapse or bifurcation point (x_0, y_0, λ_0) , where λ_0 typically corresponds to the maximum loading level or loadability

margin in P.U., %, MW or MVA depending on how the load variation are defined. Based on bifurcation theory, two basic tools have been defined and applied to computation of this collapse point, namely, direct and continuation methods.

In voltage collapse studies, the continuation method shows many advantages, so, most of the reaserchers apply this technique to trace voltage profile at various buses of the test power system, with respect to changes of loading level λ , namely, Continuation Power Flow (CPF).

In this paper the continuation power flow algorithm with smooth changes of loading level at various buses of the system, is chossen for simulation purpose.

There are two types of FACTS devices considered in this study, namely, SVC and STATCOM. Details including basic structures and terminal characteristics of these FACTS devices are presented in the following section.

III. MODEL OF FACTS CONTROLLERS

The following general model is proposed for correct representation of SVC and STATCOM in voltage collapse studies [10].

The model includes a set of differential and algebraic equations of the form :

$$\dot{x_c} = f_c(x_c, V, \theta, u)
P = g_p(x_c, V, \theta)
Q = g_p(x_c, V, \theta)$$
(2)

Where x_c represents the control system variables, and the algebraic variables V and θ denote the voltage magnitudes and phases at the buses to which the FACTS devices are connected. Finally, the variables u represent the input control parameters, such as reference voltages or reference power flows.

Description and terminal characteristics of these FACTS devices are given in the next subsections.

A. SVC

The two most popular configuration of this type of shunt controller are the fixed capacitor (FC) with a thyristor controlled reactor (TCR) and the thyristor switched capacitor (TSC) with TCR. Among these two setups, the second (TSC-TCR) minimizes stand-by losses; however from a steady-state point of view, this is equivalent to the FC-TCR. In this paper, the FC-TCR structure is used for analysis of SVC which is shown in figure 1.

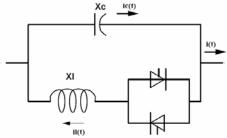


Fig. 1. Equivalent FC-TCR circuit of SVC.

The TCR consists of a fixed reactor of inductance L and a bi-directional thyristor valve that are fired symmetrically in an angle control range of 90° to 180°, with respect to the SVC voltage.

Assuming controller voltage equal to the bus voltage and performing a Fourier series analysis on the inductor current wave form, the TCR at fundamental frequency can be considered to act like variable inductance given by [7], [9]:

$$X_V = X_L \frac{\pi}{2(\pi - \alpha) + \sin 2\alpha} \tag{3}$$

Where, X_{L} is the reactance caused by the fundamental frequency without thyristor control and α is the firing angle. Hence, the total equivalent impedance of the controller can be represented as:

$$X_e = X_c \frac{\pi / r_x}{\sin 2\alpha - 2\alpha + \pi (2 - \frac{1}{r_x})}$$
 (4)

Where $r_x = X_C / X_L$. The limits of the controller are given by the firing angle limits, which are fixed by design. The typical steady-state control law of a SVC used here is depicted in figure 2, and may be represented by the following voltage-current characteristic:

$$V = V_{ref} + X_{SL}I \tag{5}$$

where V and I stand for the total controller RMS voltage and current magnitudes, respectively, and V_{ref} represents a reference voltage.

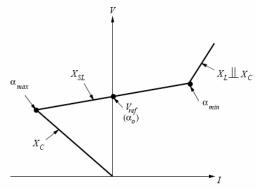


Fig. 2. Typical steady state V-I characteristic of a SVC.

Typical values for the slope X_{st} are in the range of 2 to 5%, with respect to the SVC base; this is needed to avoid hitting limits for small variations of the bus voltage. A typical value for the controlled voltage range is $\pm 5\%$ about V_{reg} [7]. At the firing angle limits, the SVC is transformed into a fixed reactance.

B. STATCOM

STATCOM is the Voltage-Source Inverter (VSI), which converts a DC input voltage into AC output voltage in order to compensate the active and reactive power needed by the

system [10]. Figures 3 and 4 show the Basic structure and Typical steady state V–I characteristic of STATCOM, respectively. From figure 3, STATCOM is a shunt-connected device, which controls the voltage at the connected bus to the reference value by adjusting voltage and angle of internal voltage source. From figure 4, STATCOM exhibits constant current characteristics when the voltage is low/high under/over the limit. This allows STATCOM to delivers constant reactive power at the limits compared to SVC.

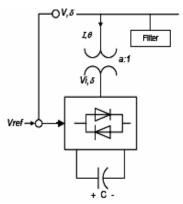


Fig. 3. Basic structure of STACOM.

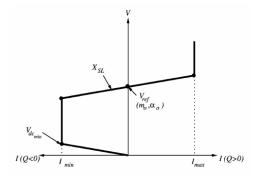


Fig. 4. Typical steady state V-I characteristic of a STATCOM.

The AC circuit is considered in steady-state, whereas the DC circuit is described by the following differential equation, in terms of the voltage V_{dc} on the capacitor [8]:

in terms of the voltage
$$V_{dc}$$
 on the capacitor [8]:
$$V_{dc} = \frac{P}{CV_{dc}} - \frac{V_{dc}}{R_c C} - \frac{R(P^2 + Q^2)}{CV^2 V_{de}}$$
 (6)

The power injection at the AC bus has the form:

$$P = V^{2}G - KV_{dc}VG\cos(\theta - \alpha) - KV_{dc}VB\sin(\theta - \alpha)$$
(7)

$$Q = V^{2}B - KV_{dc}VB\cos(\theta - \alpha)$$

$$- KV_{dc}VG\sin(\theta - \alpha)$$
(8)

Where $k = \sqrt{3/8m}$.

IV. SIMULATION RESULTS

A IEEE 14-bus test system as shown in figure 5 is used for

voltage stability studies. The test system consists of five generators and eleven PQ bus (or load bus). The simulation use a PSAT simulation software [11]. PSAT is a power system analysis software, which has many features including power flow and continuation power flow. Using continuation power flow feature of PSAT, voltage stability of the test system is investigated.

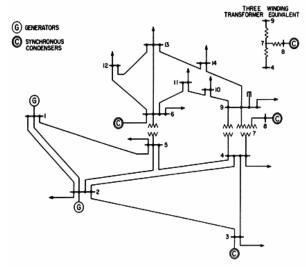


Fig. 5. The IEEE 14-bus test system

The behavior of the test system with and without FACTS devices under different loading conditions is studied. The location of the FACTS controllers are determined through bifurcation analysis. A typical PQ model is used for the loads and the generator limits are ignored. Voltage stability analysis is performed by starting from an initial stable operating point and then increasing the loads by a factor λ until singular point of power flow linearization is reached. The loads are defined as:

$$P_{L} = P_{L0}(1+\lambda)$$

$$Q_{L} = Q_{L0}(1+\lambda)$$
(8)

where P_{L0} and Q_{L0} are the active and reactive base loads, whereas P_L , and Q_L , are the active and reactive loads at bus L for the current operating point as defined by λ .

From the continuation power flow results which are shown in the figure 6, the buses 4, 5, 9 and 14 are the critical buses. Among these buses, bus 14 has the weakest voltage profile. Figure 7 shows PV curves for 14-bus test system without FACTS. The system presents a collapse or Maximum Loading Point, where the system Jcobian matrix become singular at $\lambda_{\text{max}} = 3.97295 \, p.u$. Based on largest entries in the right and left eigenvectors associated to the zero eigenvalue at the collapse point, bus 14 is indicated as the "critical voltage bus" needing Q support.

Voltage magnitude in MLP in bus 14 that is known as the weakest bus is $0.68833 \ p.u.$

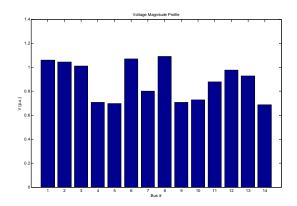


Fig. 6. Volatage magnitude profile for 14-bus test system without FACTS.

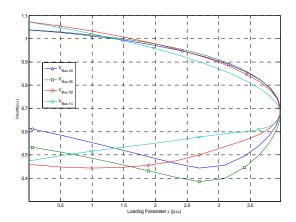


Fig. 7. PV curves for 14-bus test system without FACTS.

A. SVC

Based on collapse analysis bus 14 is targeted as the first location for an SVC. The results of locating the SVC at the desired bus are depicted in the voltage profile of figure 8. The new maximum loading level in this condition is $\lambda_{\text{max}} = 4.08238 \, p.u$. To show that maximum loading margin doesn't increase as much when the same SVC is moved to a bus that doesn't belong to the critical voltage buses e.g. for bus No. 4 $\lambda_{\text{max}} = 4.06984 \, p.u$. drive (figure 9).

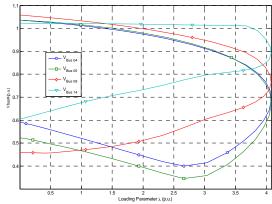


Fig. 8. PV curves for 14-bus test system with SVC at bus 14.

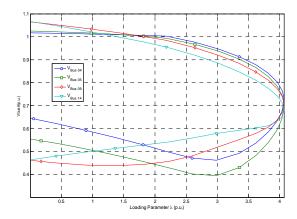


Fig. 9. PV curves for 14-bus test system with SVC at bus 4.

B. STATCOM

Then, remove the SVC, and insert the STATCOM at the bus 14 which the lowest the critical point and repeat the simulation. When STATCOM is connected at bus 14. We can observe from figure 10 that bus 14 has a flatter voltage profile. The Maximum Loading Point is increasing further at $\lambda_{\text{max}} = 4.0892 \ p.u$. It is noticed that bus 5 is the next weakest bus if the STATCOM is introduced at bus 14.

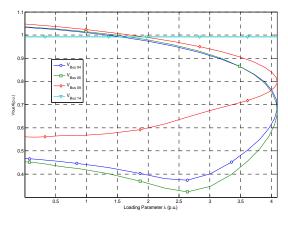


Fig. 10. PV curves for 14-bus test system with STATCOM at bus 14.

C. Comprisiaon SVC and STATCOM

PV curves of base case and system with SVC and STATCOM, are illustrated in figure 11. it indicates that with the application of SVC and STATCOM, voltage profile in bus 14 has improved significantly. At first that system experiences light load, the voltage profile of this bus with SVC and STATCOM is the same. In this condition SVC and STATCOM operate in linear region of their V-I characteristics. When the load of the system is increased, the effect of STATCOM in improving the voltage is more adequate than the SVC. When the maximum limit is reached, the SVC behaves exactly like a fixed shunt capacitor.

In maximum load condition or MLP, the magnitude of the bus no.14 voltage reaches to 0.88987 *p.u.* (with SVC) and reaches to 0.99237 *p.u.* (with STATCOM) from 0.68833 *p.u.*

(without FACTS).

It is obviously from figure 12 that the MLP of the system with STATCOM is highest while that without FACTS is lowest. Voltage reduction is lowest in case of STATCOM. From the figure, it is obvious that STATCOM gives the maximum loading margin compared to other devices.

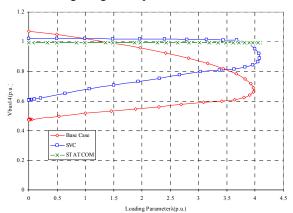


Fig. 11. Voltage profile for bus 14 with and without SVC & STATCOM at bus14.

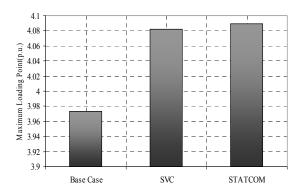


Fig. 12. Maximum Loading Point with various FACTS devices.

A snap shot of voltage profile at all the busses with different controllers are given in figure 13 at the Maximum Loading Point. Notice with SVC, STATCOM keep all busses within the acceptable voltage range.

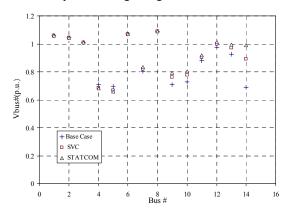


Fig. 13. Voltage profile of each bus at the MLP of system with and without SVC and STATCOM.

Using of SVC and STATCOM give the view of voltage decline before entering to the collapse point. The SVC and STATCOM significantly affects the shape of the PV curve, which improves the critical point without masking the nose point by only shift out the PV curve.

From figure 13, STATCOM provides a better voltage profile at the collapse point at bus 14 compared to other FACTS devices. This is due to the reason that the STATCOM is installed at the weakest bus. Reactive power support at the weakest bus provides better voltage profiles throughout the system. STATCOM introduces reactive power at bus 14, which improves voltage profile in its vicinity.

Voltage magnitudes at load buses 4, 5 of the system is lower in case of STATCOM compared to SVC. Voltage magnitudes at load buses 7, 9, 10, 11, 12, 13 and 14 of the system is better in case of STATCOM compared to SVC.

V. CONCLUSIONS

A comparison study of SVC and STATCOM in static voltage stability margin enhancement is presented. SVC and STATCOM increase static voltage stability margin and power transfer capability. In this paper adequate models for the SVC and STATCOM in the steady-state studies are presented and thoroughly discussed. Hence, a technique to identify the optimal placement of the FACTS devices and related equations are derived. The results of simulations on the IEEE 14 bus test system have clearly shown that how SVC and STATCOM devices increased the buses voltage, power limits, line powers, and loading capability of the network. The results of simulations also show that with the insertion of STATCOM, improving these parameters and steady-state stability of the system is more than the case when the SVC is inserted in the system.

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