# A Survey on Resource Management in IoT Operating Systems

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ABSTRACT Recently, the Internet of Things (IoT) concept has attracted a lot of attention due to its capability to translate our physical world into a digital cyber world with meaningful information. The IoT devices are smaller in size, sheer in number, contain less memory, use less energy, and have more computational capabilities. These scarce resources for IoT devices are powered by small Operating Systems (OSs) that are specially designed to support the IoT devices' diverse applications and operational requirements. These IoT OSs are responsible for managing the constrained resources of IoT devices efficiently and in a timely manner. In this paper, discussions on IoT devices and OS resource management are provided. In detail, the resource management mechanisms of state-of-the-art IoT OSs, such as Contiki, TinyOS, and FreeRTOS, are investigated. The different dimensions of their resource management approaches (including process management, memory management, energy management, communication management, and file management) are studied, and their advantages and limitations are highlighted.

**INDEX TERMS** Internet of Things, Operating Systems, Resource Management, Contiki, TinyOS, FreeRTOS.

### I. INTRODUCTION

The demands on Internet of Things (IoT) technologies have grown rapidly due to the various application fields and the advancements in wireless communications technologies [1], [2]. The term things in the Internet of Things is a piece of equipment having a sensing, actuating, storage, or processing capability. These devices possess unique characteristics, i.e., little memory, reduced battery capacity, and limited processing power [3]. The IoT has great potential to impact our lives in the future. From home automation to healthcare systems, the IoT has numerous applications to improve industries and society by enabling smart communication between objects and devices in a cost-effective manner [4], [5]. Therefore, it is predicted that there will be about 50 billion IoT devices by 2050 [6]. Due to the expansion of IoT networks in the last decade, various hardware platforms have been developed to support IoT sensors and actuators. Similarly, a number of operating systems (OSs) have gradually been developed to run these tiny sensors [7].

Various IoT communications standards have emerged from different organizations. For example, the Internet Engineering Task Force (IETF) [8], the International Telecommunication Union-Telecommunication (ITU-T) [9],

the Institute of Electrical and Electronics Engineers (IEEE), the European Telecommunications Standards Institute [10], the International Organization Standardization (ISO) and the International Electrotechnical Commission (IEC) [11], One Machine-to-Machine (M2M) [12] and the 3rd Generation Partnership Project (3GPP) [13] are actively working to provide efficient IoT communications protocols. The IETF currently has various working groups (WGs) that deal with IoT-related protocols on any layer above the link layer (e.g., at the network layer). The IETF Routing over Lossy and Low-Power Network (ROLL) WG (RFC 6550) [14] is focused on providing standardization of the IPv6 Routing Protocol for Low-Power and Lossy Networks (RPL). Similarly, the IETF IPv6 over Low-Power Wireless Personal Area Networks (6LoWPAN) (RFC 4944) [15] works on IPv6 networking protocol optimization using IEEE 802.15.4. The IETF 6loBAC WG (RFC Ed Queue) [16] provides specifications for transmission of IPv6 packets on master-slave/token-passing (MS/TP) networks. The IETF 6TiSCH Operation Sublayer (6TOP) WG (RFC Ed Queue) [17] defines the mode of operation for IPv6 using an IEEE 802.15.4e (6TiSCH) network. Datagram Transport Layer Security (DTLS) in a Constrained Environment (DICE) was

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drafted by the IETF WG DICE (RFC 7925) [18]. At the application layer, the IETF Constrained Application Protocol (CoAP, RFC 7252) provides web services to constrained devices [19]. Concise Binary Object Representation (CBOR, RFC 7049) provides binary representation of structured data [20]. The Object Signing and Encryption (COSE) WG (RFC Ed Queue) focuses on creating CBOR-based signing and encryption formats [20]. Application layer security for data exchange with CoAP using the COSE format is provided by IETF's Object Security of CoAP (OSCoAP, RFC 7744) [21].

Similarly, ITU-T provided an overview of the IoT and its reference model [22]. ITU-T Task Group 15 (TG-15) is working on smart grid communications aspects of the IoT [23]. Similarly, ITU-T TG 17 is focusing on security and identity management aspects of the IoT [24]. An ISO/IEC joint technical committee does not develop standards but it provides current and future IoT trends and requirements [25]. The IEEE defines an architectural framework for the IoT [26]. The IEEE P2413 working group provided a description of "various IoT domains, definitions of IoT domain abstractions, and identification of commonalities between different IoT domains" [27]. The IoT IEEE 802.15 working group is dealing with medium access control (MAC) and physical layer specifications for wireless personal area networks (WPANS), a mesh topology capability in WPANs, and shortrange wireless optical communications using visible light. ETSI has developed a low-throughput network (LTN) as a wide area network (WAN) for the IoT [28]. One M2M is a standardization body that consists of eight world standard development organizations. Their goal is to develop a common standard for M2M communications. 3GPP is also working to meet IoT requirements [29]. LTE Release 12 [30] from 3GPP provides a power-saving mode and a lower overhead signaling procedure to provide energy efficiency [31]. An IoT OS should be flexible enough to support these protocols without violating the needs of resource-constrained tiny devices.

IoT devices have limited memory and power and require real-time capabilities in some scenarios. Additionally, they should support heterogeneous hardware along with efficient connectivity and security mechanisms [32]. Connecting and operating this huge number of devices in an efficient way is one of the most important design goals for the research community. In an IoT system, the fundamental research issue is to manage the available resources in an ordered and controlled manner. The ultimate objective of an IoT resource management mechanism is to satisfy IoT device requirements efficiently [33].

IoT devices are classified into two general categories; i.e., high-end IoT devices and low-end IoT devices [7]. High-end devices contain more processing power and energy, such as smartphones and Raspberry Pi. The low-end devices, on the other hand, are too resource-constrained. Therefore, a traditional OS, such as Linux, cannot run these small resource-constrained devices. Hence, the IoT cannot achieve

TABLE I. List of abbreviations.

TABLE I. LIS	t of abbreviations.					
Symbols	Description					
3GPP	3rd Generation Partnership Project					
6LoWPAN	Low-power Wireless Personal Area Networks					
6TOP	6TiSCH Operation Sublayer					
AEON	Accurate Prediction of Power Consumption					
BVR	Beacon Vector Routing					
CBOR	Concise Binary Object Representation					
CCA	Clear Channel Assignment					
CFS	Coffee File System					
СН	Cluster Head					
CoAP	Constrained Application Protocol					
COSE	Object Signing and Encryption					
DICE	DTLS in Constrained Environment					
DIO	DODAG Information Message					
DODAG	Destination-oriented Directed Acyclic Graph					
DSVR	Destination Sequence Vector Routing					
DTLS	Datagram Transport Layer Security					
DYMO	Dynamic MANET on Demand					
EATT	Energy-aware Target Tracking					
ELF	Efficient Log-structured Flash					
ERTP	Energy-efficient and Reliable Transport Protocol					
ETSI	European Telecommunications Standards Institute					
HAL	Hardware Abstraction Layer					
HDRTP	Hybrid and Dynamic Reliable Transport Protocol					
ICMP	Internet Control Message Protocol					
IETF	Internet Engineering Task Force					
IGMP	Internet Group Management Protocol					
ISO/IEC	International Organization for Standardization and International Electrotechnical Commission					
ITU-T	International Telecommunication Union- Telecommunication					
LAD	Location Aided Routing					
LEACH	Low Energy Adaptation Clustering Hierarchy					
LPL	Low Power Listening					
LTN	Low Throughput Network					
MS/TP	Master-Slave/Token-Passing					
OF	Objective Function					
OSCoAP	Object Security of CoAP					
POSIX	Portable Operating System Interface for Unix					
RCRT	Rate Controlled Reliable Transport Protocol					
ROLL	Routing over Lossy and Low-Power Network					
RPL	Routing Protocol for Low-Power and Lossy Networks					
STCP	Stream Control Transmission Protocol					
STI	Software Thread Integration					
TORP	TinyOS Opportunistic Routing Protocol					
TOS-PRO	TinyOS Preemptive Original					
TOSSTI	TinyOS Software Thread Integration					
TOSThread	TinyOS Thread					
UTOS	Untrusted Extension for TinyOS					
<u> </u>	<u> </u>					

its full potential until there is a de facto standard OS providing support to run these low-end devices across a

heterogeneous network [6]. Moore's law [34] is not applicable to IoT devices in terms of processing power. However, it can be applied to device size and energy efficiency [35]. The low-end devices possess very little random access memory (RAM) and few processing capabilities. The IETF [36] developed the IPv6 over Lowpower Wireless Personal Area Networks (6LoWPAN) standard adaptation layer to enable low-power low-data-rate communications [37]. These devices also require real-time capabilities in scenarios like vehicular communications, health care systems, and factory automation and surveillance applications. Providing communications with energy efficiency and reliability is the main objective of the IoT. IoT low-end devices contain small amounts of memory and little processing power. Therefore, in order to satisfy low-end device resources, choosing a suitable lightweight OS is of vital importance. Several OSs have been proposed by numerous companies that offer a different approach to fundamental problems.

The future IoT environment needs to handle and perform tasks independently. Similarly, an ultra-dense network yields computational complexity. In order to cope with IoT low-end-device challenges, such as limited resources, and distributed and dense environments, there is considerable need for an efficient resource management mechanism in an IoT OS [38]. An IoT OS is primarily responsible for managing the device's resources efficiently. Various OSs have presented different solutions to satisfy low-end devices' resource needs. To achieve this goal, various mechanisms are

provided by the different OSs to provide proper functioning of sensing nodes. Among various proposed OSs for low-end devices, the Contiki [39], TinyOS [40], and FreeRTOS [41] are most prominent for operating in a resource-starved network. IoT low-end devices usually operate with limited battery power. Consequently, providing an energy-efficient OS is of the utmost importance [42]. These low-end devices transfer the sensed data using a communications protocol. In order to be energy-efficient, the communications protocols should save the maximum amount of energy. Protocols at the transport layer, MAC layer, and network layer need to be energy-efficient [43], [44], [45].

IoT devices require computational capabilities for their sensing operations. These constrained sensing motes do not offer extensive memory and processing capabilities (usually 100 kB flash memory and 10 kB RAM). For example, Crossbow's Telos B mote provides only 10 kB of RAM and 48 kB of flash memory. Due to this limitation, IoT devices need to manage their resources efficiently. Additionally, densification, randomness, and uncertainty make IoT device resource management a challenging task. An OS acts as a resource manager for this complex IoT system [46]. To handle the limited processing power and memory, an OS requires an effective process and memory management mechanism. IoT devices are battery-operated and are mostly deployed in remote environments. Thus, energy management provided by an OS is highly important. The main objective of an IoT system is to provide a sensing operation and transfer the sensed data to the base station for further processing. The

TABLE II. Overview of comparison between this study and available surveys.

Approaches	Key Concepts	This Study		Hahm et al. [7]		Amjad et al. [47]		Strazdins et al. [50]		Reusing et al. [51]		Farooq et al. [53]							
		C	T	F	C	T	F	C	T	F	C	T	F	C	T	F	C	T	F
Process Management	Programming Model	✓	<b>√</b>	✓	✓	✓	✓		✓			✓		✓	✓		✓	✓	
	Scheduling Model	<b>~</b>	<b>✓</b>	<b>√</b>	<b>√</b>	<b>✓</b>	✓		<b>~</b>					<b>✓</b>	<b>✓</b>		<b>✓</b>	<b>√</b>	
Memory Management	Memory Allocation/De- allocation	<b>√</b>	<b>√</b>	<b>√</b>	<b>√</b>		<b>√</b>		<b>√</b>						<b>✓</b>		✓	<b>√</b>	
	Memory Fragmentation and Safety	<b>✓</b>	<b>√</b>	<b>√</b>			<b>√</b>		<b>✓</b>								✓	<b>~</b>	
Energy Management	Software-level Energy Management	<b>~</b>	<b>~</b>	<b>√</b>					<b>✓</b>		<b>√</b>	<b>✓</b>			<b>✓</b>				
	Energy Tracking		✓	✓					<b>^</b>						✓				
Communication Management	Supported MAC Layer Protocols	<b>√</b>	<b>√</b>	<b>√</b>	<b>√</b>		<b>√</b>		<b>√</b>										
-	Supported Network Layer Protocols	<b>√</b>	<b>√</b>	<b>√</b>	<b>√</b>		✓		✓								✓	✓	
	Supported Transport Layer Protocols	<b>√</b>	<b>√</b>	<b>√</b>					✓								✓	<b>√</b>	
File Management	Storage Abstraction	<b>√</b>	<b>√</b>	<b>√</b>													✓	<b>√</b>	
	File Storage and Organization	<b>√</b>	✓	<b>√</b>													<b>√</b>	<b>√</b>	

C: Contiki, T: TinyOS, F: FreeRTOS

communications design, signal processing, data reception, data transmission, and radio sleep/wake mechanism need to be efficient in terms of energy and communications. IoT OSs store, catalog, and retrieve the data using a file system. Therefore, the provision of an efficient, robust, and appropriate file system is highly desirable in IoT OSs.

Moreover, an IoT OS should be highly concurrent to support these low-end-device sensing operations. Hence, the importance of efficient resource allocation in the OS for lowend devices motivates us to write this paper. In this paper, we consider the IoT low-end device resource management solutions offered by various OSs. To the best of our knowledge, this is the first paper that encompasses detailed information about the resource management mechanisms in Contiki, TinyOS, and FreeRTOS. Various resource management operations, including process management, memory management, energy management, communications management, and file management (and their advantages) are discussed in order to make the low-end devices more and more resource-efficient and flexible. Thus, this study has taken all the resource management mechanisms into consideration. A list of abbreviations is provided in Table I, whereas Table II provides a comparison of this study and already existing surveys on tiny sensor device OSs.

The contributions of this paper compared to the recent literature in the field are as follows.

- a. It provides a literature review related to IoT OSs.
- b. It covers the resource management aspects of Contiki, TinyOS, and FreeRTOS, including:
  - process management
  - memory management
  - energy management
  - communication management, and
  - file management
- c. It provides future research directions and challenges in resource management of IoT OSs.

The remainder of this paper is structured as follows. Section II provides an overview of related work. Section III discusses resource management classifications in detail. Section IV provides open research issues and recommendations, followed by Section V, which concludes the paper.

# II. RELATED WORK

Over the years, several OSs for the IoT have emerged. Contiki, TinyOS, and FreeRTOS emerged as predominant OSs to provide support to IoT devices. This section discusses the recent survey papers related to IoT OSs, e.g., Hahm et al. provided a detailed analysis of various requirements to satisfy low-end IoT devices [7]. The survey discussed various OSs that could become the de facto standard. OSs in this survey are categorized into three types, including event-driven OSs, multithreading OSs, and pure real-time operating systems (RTOSs). Along with the key design choices, the characteristics of each category are presented in the study. Based on the key design choices and low-end device

requirements, the most prominent OS representing each category is identified.

Similarly, Amjad et al. discussed several aspects of TinyOS design in detail [47]. This survey encompassed the design paradigm and main features of TinyOS. It has eventdriven concurrency, a programming layout based on NesC (a dialect of the C programming language), a monolithic architecture, and a non-preemptive task scheduler. TinyOS memory management, energy management, and energyefficient communications protocols were presented. TinyOS uses a software thread integration (TOSSTI) mechanism for energy conservation, which helps an OS utilize busy-wait time in an efficient manner [48]. Similarly, an energy tracking mechanism is also utilized by TinyOS [49]. To maintain network stability and lifetime, TinyOS supports several communications protocols at the transport layer, MAC layer, and network layer. In addition, simulators for TinyOS and its various sensing applications are also discussed in the paper.

Strazdins et al. surveyed wireless sensor network (WSN) deployments and analyzed the collected data to study the design rules for a WSN OS using 40 deployment scenarios [50]. Deployments from 2002 to 2011 are reviewed to study different WSN applications, including environmental monitoring, animal monitoring, human-centric applications, infrastructure monitoring, smart buildings, and military applications. The authors studied Contiki, TinyOS, LiteOS, and MansOS, and proposed 25 design rules. The rules include suggestions related to the task scheduler, networking protocol, and energy-efficiency mechanism.

TinyOS and Contiki are the two best-known OSs for low-end devices. A comparison between these two OSs is presented in a survey by Reusing [51]. The main requirements an OS should fulfill for a sensor network include concurrency, flexibility, and energy efficiency [52]. The contrast between TinyOS and Contiki is shown based on these requirements. Special emphasis was placed on a programming model and execution model, along with the hardware platforms supported by both OSs. This survey indicates that TinyOS might be more useful in a resource-constrained environment, whereas Contiki provides more flexibility in the network.

Farooq and Kunz highlighted major challenges for an OS design, and identified the advantages and limitations in an OS for a WSN [53]. For example, Contiki follows a modular kernel concept. It is a layered approach in which application modules are independent and can be linked with a kernel at boot time. In this way, the kernel provides only core services, while other services can be added when required. Hence, it reduces the memory footprint and decreases boot time. However, the kernel may crash due to modules that contain bugs. Similarly, TinyOS follows a monolithic architecture similar to Linux. The monolithic architecture helps to reduce modular interaction costs. However, it may make the OS unreliable and hard to maintain, because no clear boundaries are provided

between modules. The alternative is a microkernel architecture.

FreeRTOS is an example of a microkernel structure which provides robustness against bugs in the components. The microkernel provides minimum functionalities to the kernel. Hence, the kernel size is reduced significantly. All other functions are provided by servers running at the user level. Therefore, OS functionalities are extendable, and failure in one user-level server does not crash the kernel itself. The disadvantage is poor performance due to user-toboundary crossing. The OS architecture, programming model, scheduling mechanism, memory management, and communications protocol support a major design goal for an IoT OS. Along with resource sharing, support for real-time applications is of vital importance. It is pointed out that some OSs support a priority application capability, whereas few provide real-time applications. Some miscellaneous features, including communication security, file system support, simulation support, and programming language, are also discussed.

Dariz et al. compared Contiki and TinyOS with a realtime OS named ChibiOS to study the safety-relevant application in a WSN [54]. Another OS called LiveOS was introduced to conserve memory and energy for a WSN [55]. Over the years, various OSs have emerged in the WSN community. One of the critical issues for an OS is dealing with a large number of resources to provide ubiquitous services to IoT low-end devices [56]. In this study, we focus on three well-known OSs: Contiki, TinyOS, and FreeRTOS. This study aims to cover all the resource management aspects of a low-end-device OS.

## III. RESOURCE MANAGEMENT CLASSIFICATION

OS provides a layer of abstraction for the hardware by managing the resources on each IoT device [57]. The OS provides a programming interface and manages processor time. IoT devices operate in resource-constrained

application, a suitable execution model must be provided by OS. The execution model must provide memory efficiency [58]. Similarly, OS (being battery-powered) must provide a sleep mode when no application is running [59]. Providing energy efficiency to the communication components is more challenging for an OS. The communication components must wake up during a communication period. Therefore, an OS handles energy efficiency during communication using various mechanisms, for example, a separate radio duty cycling procedure [60], a virtual carriersensing mechanism with a network allocation vector, and time-division multiple access (TDMA)-based methods. Not all IoT devices have storage like flash memory. Therefore, an appropriate file system is required to provide storage needs for some applications. The file system needs to efficiently map the data into sectors to make writing and reading of data more efficient. Therefore, an OS must provide a full file system interface [61] [62].

The communications needs of diverse applications are handled by a communications architecture. Considering the device's resource scarcity, the communications protocols must be energy- and memory-efficient during data collection. event detection or tracking, synchronization, neighbor discovery, and data delivery [63] [64]. To address the resource management challenges for low-end IoT devices, various resource management mechanisms and schemes have been proposed. These resource management schemes fall into five subsections. The flow chart of resource management in OS for low-end devices is shown in Figure 1.

## A. PROCESS MANAGEMENT

In the context of resource management, the kernel manages processes and threads to share information, protect process resources, and assign system resources in a safe way. In the IoT environment, multiple activities may occur during a certain time period. Managing these activities and processes

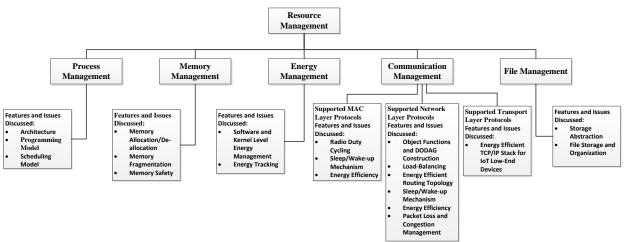


FIGURE 1. Resources management classification.

concurrent environments, and to handle this concurrent

by fairly sharing resources is essential, and it depends on the OS execution model.

The Contiki and TinyOS follow an event-driven execution model to provide memory efficiency and low complexity of state machines in the event-driven TCP/IP stack [65] [66]. Event handlers continuously wait for internal or external events, such as an interrupt. The kernel allocates the memory stack to the process, and an event handler follows the run-to-completion mechanism. All the processes effectively share the same stack and utilize limited memory efficiently. Some events are queued and processed in first in, first out (FIFO) fashion. The eventdriven concurrency model introduces certain complexities if multiple events occur. The task in an event-driven model cannot be blocked during run-time. Sometimes, timecritical tasks need to be executed first. Therefore, real-time performance of the event-driven approach is poor. Hence, an OS needs multiple event handlers.

Low-end IoT devices offer only few kilobytes of RAM. The multithreading approach allocates a stack of memory to each thread even if the thread is not utilizing memory. Hence, most of this memory is unused. Therefore, a more effective hybrid model is required for better memory efficiency and low programming complexity. The Contiki supports a novel, lightweight, stackless threading mechanism called a protothread [67]. The protothread utilizes a multithreaded model without increasing multiple-stack overhead. In an event-driven approach, the program runs to completion, which is not desirable in some

scenarios, especially in a system where a high-priority task is present. A protothread simplifies the event-driven programming model by providing a conditional locking wait statement that enables a program to execute a blocking wait without introducing an additional stack for each protothread. Between the beginning and end of each protothread, there is a conditional wait statement. This conditional wait statement blocks the program if there is an interruption. In other words, the thread is blocked only if an explicit blocking wait statement is used. In this way, the number of explicit state machines in the event-driven approach is reduced, with memory overhead of only two bytes per protothread. The protothread is a better alternative for memory efficiency. However, providing process synchronization between protothreads is not possible.

Sometimes blocking certain components may interrupt the whole sensing application. TinyOS Thread (TOSThread) is a complete implementation of a preemptive application-level thread library to achieve maximum concurrency without increasing resource usage [68]. TOSThreads categorize all event-based code into kernel-level threads and application-level threads. Kernel-level threads are given the highest priority, and cannot be interrupted by application-level threads. An application-level thread makes a system call application programming interface (API) that does not interrupt the TinyOS code itself; rather, it sends a message to the kernel thread. Application-level threads execute only if kernel-level threads are not active. The basic architecture of a TOSThread is shown in Figure 2.

Relevant References	os	Architecture	Category	Scheduling	Programming Language	Key Concept	Advantages	Limitations
[65], [67],	Contiki	Monolithic	Event- Driven	Cooperative	C	Protothread: Provides conditional locking wait statement and utilizes multithreaded model without increasing multiple stack overhead.	Module interaction cost is low.	Need proper process synchronization.
[66], [68], [69]	TinyOS	Monolithic	Event- Driven	Cooperative	NesC	TOSThreads: Categorizes all event- based code into synchronous (tasks) and asynchronous (interrupts) execution context.	Provides maximum concurrency without increasing resources usage.	Cannot provide proper execution of abnormal tasks.
[70], [71], [72]	FreeRTOS	Microkernel	Real-time	Preemptive	С	Uses microkernel to provide OS reliability.	Time tracking mechanism to disable a periodic tick is beneficial.	Cannot handle large IoT system tasks.

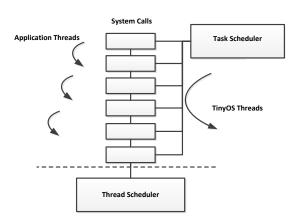


FIGURE 2. TOSThreads architecture (adapted from [68]).

The overall structure consists of five elements: a single kernel-level thread, a number of application-level threads, a task scheduler, a thread scheduler, and system-call APIs. A number of application threads run concurrently and make a call to the kernel-level threads through API slots. The thread scheduler provides concurrency between application-level threads and system-call APIs. TOSThread provides a preemptive behavior to TinyOS but increases the computational complexity. To provide preemptive execution in a simple manner, the TinyOS preemptive original (TOS-PRO) approach was introduced [69]. This approach provides increased flexibility for scheduling without introducing extra complexity into TinyOS.

FreeRTOS is based on a microkernel architecture and utilizes a multithreading approach [70]. Each process can be interrupted, and the scheduler can switch between threads [71]. It provides a real-time, preemptive multitasking environment for low-end devices. It ensures execution of a higher priority task in any given time period. If two tasks are given equal priority, the scheduler divides execution time between them. This execution follows a priority-based round-robin implementation. The FreeRTOS kernel is structured using four C files (task.c, list.c queue.c and croutine.c), where task.c provides scheduling functionalities by using structures and functions in the list.c file. The queue.c file provides a thread-safe queue to implement inter-task communication and synchronization, and croutine.c implements simple lightweight tasks [72]. The IoT OS process management overview is given in Table III.

### **B. MEMORY MANAGEMENT**

Memory management provides techniques for allocating and deallocating memory for various processes and threads. OS offers two common methods for memory allocation, i.e., static allocation and dynamic allocation. In static memory management, OS allocates memory to the system that cannot be altered during run-time. But a dynamic management technique provides flexibility in memory acquisition at run-time. Static allocation cannot predict how much memory will be needed, especially in real-time

scenarios. Similarly, memory over-provisioning may result in memory overhead. With dynamic allocation, if the allocated memory is not freed, it may result in a memory leak.

The memory size for sensor devices is constrained due to the device's physical size and the cost. Static memory contains the program code, and dynamic memory contains run-time variables, the buffer, and the stack. IoT low-end devices as classified by IETF require about 10 kB of RAM and about 100 kB of flash memory. The Contiki C library provides a set of functions for allocation and de-allocation of memory for the heap. For example, the memb macro(), and memb\_alloc(), and memb\_free() functions are used for declaration, allocation, and de-allocation, memory respectively [73]. The memory allocation function needs to handle memory fragmentation. If memory is fragmented, allocation may fail to allocate all the unused memory. The managed memory allocator function mmem() in the Contiki frees the allocated memory from fragmentation by compacting it when blocks are unused. However, dynamic allocation may lead to stack overflow, and requires more space. TinyOS is based on the NesC programming language [74]. To cope with sensor node hardware constraints, the language does not support dynamic memory allocation, the program states and memory are declared at compile time. In this way, memory fragmentation and run-time allocation failure are prevented. Similarly, maintaining an additional data stack to manage the dynamic heap is not required [75]. In the earlier version of TinyOS, the basic building block (i.e., memory safety) was not available [76]. However, new updates and revisions provided memory safety and memory safety-check features. Safe TinyOS was developed mainly to provide memory safety to sensor nodes [77]. Similarly, Untrusted Extension for TinyOS (UTOS) utilizes a sandboxing concept to provide enhanced memory safety features, compared to Safe TinyOS [78]. To provide memory safety features to memory-constrained devices, CCured is leveraged [79]. CCured provides a red line that draws a boundary between trusted and untrusted extensions. The untrusted extensions cannot access the hardware and network resources directly. An extension communicates with the rest of the system through a proper UTOS system call interface. The extension is terminated if it violates the safety model of the system. The CCured compiler inserts dynamic safety checks before every operation.

Restarting an extension is still faster than rebooting a TinyOS application. To make the memory more efficient, unstacked C is used, which is a source-to-source transformation to translate a TinyOS multithread program into stackless threads. Since these programs do not have a separate stack, their memory overhead is reduced significantly. Dynamic memory-like capabilities can be offered in TinyOS by using a component named TinyAlloc through an interface called MemAlloc. Additional memory management and capacity are provided through a TinyPaging mechanism, which makes use of flash storage [77]. TinyAlloc allows double referencing, which means

Relevant References	OS	Туре	Key Concept	Advantages	Limitations
[39], [51], [73]	Contiki	Dynamic	MEMB() macro, memb_alloc(), memb_free() functions are used to declare, allocate and deallocate memory. mmem() function frees the memory from defragmentation.	Offers memory size, dynamically adjusting capabilities for changing requirements during run- time.	Does not provide Memory Protection Unit (MPU).
[74], [75], [76], [77], [78], [79]	TinyOS	Static/Dynamic	Program transformation system (CCured) is used to provide memory safety. Unstacked C translates multithread into stackless threads. TinyAlloc component is used to provide dynamic allocation capability. TinyPaging mechanism is used to provide additional space.	Static allocation prevents memory fragmentation and run-time allocation failures.	Does not provide memory usage prediction. Memory may get wasted if program is unused.
[80], [81]	FreeRTOS	Dynamic	pvPortMalloc() and vPortFree() funtions are used to provide three heap implementations to allocate and de-allocate.  Heap_1: Does not de-allocate the allocated memory.  Heap_2: Frees the allocated memory.  Heap_3: Allocates and de-allocates the memory similar to Contiki mechanism.	Offers several heap management schemes, depending on the application requirements.	Memory is not safe thread nor deterministic.

TABLE V. Predicted energy consumption (in mJ) and node lifetime for TinyOS 1.1.7 component (adapted from [84]).

Test Application	CPU				LEDs	Sensor Board	Total Energy	Lifetime
	Active	Idle	Rx	Tx				
Blink	0.37	601.6	0	0	196.2	-	798.2	25.8
CntToLeds	0.77	601.5	0	0	590.6	-	1193	17.4
CntToLedsAndRfm	93	560.7	1651	130	589.6	-	3025	6.9
CntToRfm	92.7	560.8	1651	130	0	-	2435	8.5
RfmToLeds	82.9	565.2	1727	0.6	589.0	-	2965	7.0
SenseToLeds	1.85	601	0	0	$0^{1}$	126	728.8	28.5
SenseToRfm	4.39	560.3	1651	130	0	126	1937	10.7

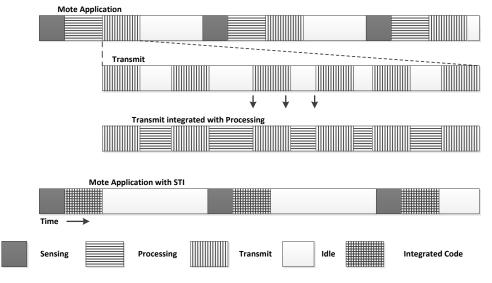


FIGURE 3. TinyOS software thread integration (TOSSTI) (adapted from [48])

that the memory region is referenced indirectly through another array that contains it current address. Hence, TinyAlloc can alter the memory address in the intermediate array, and move the memory region freely with in the heap. The MemAlloc interface in TinyAlloc returns a pointer handle to the newly assigned memory region, and also frees the memory region and returns the handle pointer to allocated memory. Tinypaging uses virtual addresses. The memory region is allocated a virtual address. Before using it, a dereferencing function takes the virtual address and returns the physical address for that memory. It also reduces the need to use an additional intermediate array. Hence, Tinypaging combines these concepts and works with virtual addresses to exchange parts of memory into flash.

The additional threads in TinyOS that provide more execution and concurrency support may require more memory usage. Therefore, memory usage prediction is required for TinyOS applications. With a real-time operating system (FreeRTOS), the kernel allocates memory dynamically for every event. The malloc() and free() functions are not desirable in a real-time operating system due to the fact that dynamic memory allocation has typically deterministic run-times, needs extra code space, and suffers from memory fragmentation. To eliminate these problems, FreeRTOS introduced two new functions: pvPortMalloc() and vPortFree() [80]. These functions provide three heap implementations for memory allocation, depending on the system design [81]. Heap\_1 does not allow de-allocation of memory once it is allocated. It is suitable for a system where allocated memory size always remains the same (for example, with application tasks that do not vary with time and that are created before the kernel is started). Heap\_2, in contrast to heap\_1, allows previously allocated memory to be freed. It does not combine adjacent free blocks into a larger memory block. This scheme is suitable for systems where tasks are created dynamically. Heap\_3 is similar to the malloc() and free() function allocations, and make a safe thread. This scheme is not memory-efficient, and may increase the kernel code size.

The memory management aspect of IoT OS is summarized in Table IV.

#### C. ENERGY MANAGEMENT

IoT devices consume energy during sensing, data processing, and data transfer. The management of limited energy has been a key issue for these devices due to the fact that these sensors are deployed mostly in remote environments and function without human intervention. Therefore, OS should provide an energy-efficient mechanism to prolong the life of an IoT network [82]. The management of a limited energy budget is rudimentary, and can be accomplished through both hardware and software techniques [83]. Hardware-based approaches require additional hardware, which increases system cost. Software-based techniques are more practical, but may introduce additional overhead. Energy efficiency can be achieved through network protocol design and OS scheduling aspects, e.g., sleep/wake and duty-cycle modes are employed in most OSs to conserve energy [64]. Reducing energy consumption through a software mechanism requires a comprehensive view of the application at a different layer of the system, and is an essential condition for OS.

The Contiki kernel offers no explicit power-saving mechanism. The applications provide a power-saving mode by utilizing an event queue size. The application can put the CPU into sleep mode when the event queue is empty [65]. The Contiki network-level energy-saving mechanisms are discussed in more detail in Section D.

TinyOS utilizes software thread integration (STI) for energy conservation [48]. The node faces idle—busy time during sensing, processing, and transmission. The idle time is too short to perform traditional context switching. With STI, the processor can reclaim this time to perform other useful tasks, as depicted in Figure 3. Similarly, the processor can boost battery life by switching to low-power mode sooner. The TinyOS overall system response time also improves, which supports higher priority task processing. In this way, the scheduler can provide the

TABLE VI. An overview of energy management.

Relevant References	OS	Key Concept	Advantages	Limitations
[65]	Contiki	Application-specific energy conservation implementation.	The applications provide a sleep mode by observing event-queue size.	No specific kernel-level power- saving mechanism is provided.
[48], [84], [85], [86]	TinyOS	Software Thread Integration (STI).  Energy-Aware Target Tracking (EATT).	With STI, the processor can boost battery life. The number of CPU cycles can be minimized.	This mechanism is not suitable for mobile devices and may introduce additional memory usage.
[88], [89], [90]	FreeRTOS	Tickless Idle.	It disables a periodic tick source for a period of time to put the processor into deep sleep mode for more energy savings.	Introduces run-time overhead.

effects of pre-emption at the task level. Hence, it enhances the concurrency model of the scheduler.

Allocating the energy dynamically by predicting the power consumption of nodes can be helpful to conserve energy. For example, accurate prediction of power consumption (AEON) is an energy prediction tool for sensor nodes [84]. TinyOS application energy prediction based on AEON is shown in Table V. Table V shows the amount of energy consumed by each component. For example, the radio consumes most of the energy, thus, the CPU idle-active mode duration can be altered to extend node lifetime. Similarly, the TinyOS programming mode supports an energy-tracking mechanism to track energy consumption of various components. An energy-aware target tracking (EATT) algorithm is implemented in TinyOS using a clustering and data aggregation technique [85]. The tracking algorithm is executed by the cluster head (CH) that performs data collection, aggregation tracking, and result propagation to send the results to the desired location. Through energy tracking optimization, the number of CPU cycles can be minimized. However, this mechanism is not suitable for mobile devices, and may introduce additional memory usage. A distributed energy-aware wake-up counter was tested in TinyOS to provide updated link status in real time [86].

In an event-driven system, the threads of executions or tasks spend a portion of their time waiting for an interrupt, or for a time period to expire. In FreeRTOS, these tasks are referred to as being in a blocked state [87]. If all the tasks are in a blocked state, FreeRTOS creates and runs a task

called idle task. Therefore, when the processor is idle, it can go into power-saving mode. This is implemented in FreeRTOS using an idle task hook function [88]. The idle task is given the lowest priority, and the idle hook function gets called only if there is no higher priority task available [89]. Hence, this function provides an automatic powersaving mechanism to the FreeRTOS processor. This mechanism may be beneficial in some scenarios, but if the frequency of the ticks is too high, the processor will waste energy and time in entering and exiting idle mode. Hence, the power savings through this mechanism are not beneficial. Therefore, to provide an appropriate powersaving mechanism, a tickless idle technique was introduced [90]. Tickless idle is a power management technique for FreeRTOS that provides more power saving during processor idle states. It uses a time-tracking mechanism to disable a periodic tick source for a period of time to put the processor into deep sleep mode until a higher priority external or kernel interrupt occurs. However, it introduces run-time overhead. The energy management aspects of IoT OSs is presented in Table VI.

#### D. COMMUNICATION MANAGEMENT

Providing seamless continuous and ubiquitous communication between IoT devices is the ultimate goal of an IoT OS. IoT networking is complicated by the devices' wireless nature, heterogeneity, density, and diverse transmission patterns [91]. Therefore, communications support at the MAC layer, the transport layer, and the network layer impacts overall IoT network performance.

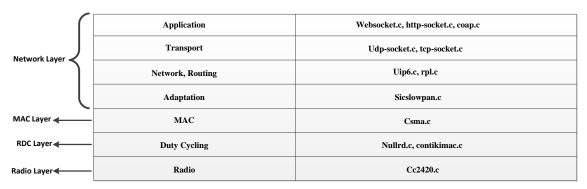


FIGURE 4. Contiki network stack (adapted from [96]).

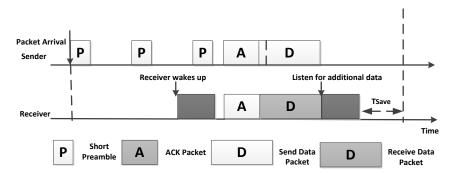


FIGURE 5. X-MAC medium access (adapted from [99]).

There is a plethora of IoT communication protocols available in the literature [92]. Some of these protocols are widely accepted and standardized. The IoT communications protocols should focus on energy efficiency rather than providing higher throughput. The networking stack for an IoT OS must support higher-level services, including data dissemination and accumulation. It also requires managing low-level services, including radio management, queue management, and MAC support [93]. Apart from these requirements, there is a need to consider the devices' unique traffic characteristics, and consequently, a need to manage the quality of service (QoS). For example, in the smart metering scenario, devices periodically transmit a small burst of data. A detailed tabular overview of communication management section is provided in Table VII.

# 1) CONTIKI SUPPORT FOR COMMUNICATION PROTOCOLS

The Contiki provides two networking stacks, i.e., a uIPv6 netstack and a Rime communications stack [94]. uIPv6 is the implementation of the TCP/IP protocol stack for eightbit microcontrollers, and can be configured with 6LowPAN, RPL routing for low-power and lossy networks, User Datagram Protocol (UDP) and Constrained Application Protocol (CoAP) [95]. Similarly, the Rime communications stack is designed for low-power radio. It supports singleunicast, single-hop broadcast, and multi-hop communications. In multi-hop scenarios, Rime allows applications to implement routing protocols other than the Rime stack-implemented protocols. The Contiki network stack layer model is shown in Figure 4. The Contiki network stack layer is a little bit different than the traditional OSI layer. It covers all the OSI layers; however, there is a radio layer, a radio duty cycle layer, and a MAC layer present in between the network layer and the physical

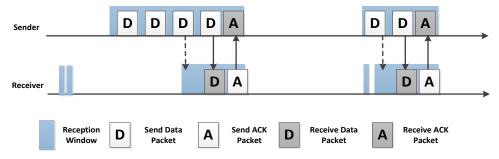


FIGURE 6. ContikiMAC mechanism of sending data packet (adapted from [100]).

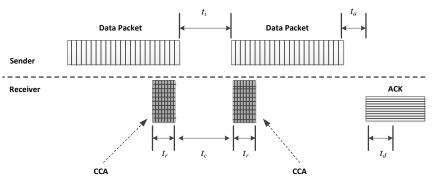


FIGURE 7. The ContikiMAC transmission and CCA timing (adapted from [100]).

TABLE VII. An overview of communication management.

Relev ant Refer	os	Commu nication Layer	Key Concept	Advantages	Limitations
[97], [98], [100], [102],	[97], Contiki MAC [98], Layer [100], [102],		ContikiMA C X-MAC	Provides an excellent sleep/wake-up mechanism.  Provides better retransmissions and archives higher PDR as compared to ContikiMAC.	Can face false and unnecessary wake-ups. Phase-lock needs to be improved. No proper collision avoidance mechanism is provided, i.e., it requires a CCA mechanism.
[105], [106], [107], [108],			CSMA- MAC	Provides a collision avoidance mechanism.	As a result, the ETX is very high.  If collision is detected; it does not pass this information the upper layer, which may affect the overall routing operation.
[109], [110], [111], [112], [113]		Network Layer	ContikiRPL	IPv6 forwarding table mechanism.	ContikiRPL uses MRHOF as an OF, which is not suitable in all application scenarios. The RPL protocol requires a proper routing metric and OF for parent selection. Interoperability is also required. Similarly, mobility is not supported.
			RER <sub>BDI</sub>	For DODAG construction, it takes both the residual energy ratio (RER) of the nodes and their battery discharge index (BDI).	Have not provided the overhead cost, load balancing, and memory footprint information.
		T	BRPL	Supports node mobility and varying traffic.	Trickletimer adjustment is an issue, especially in mobile node scenarios.
		Transport Layer	uIP	Suitable for simple TCP and UDP scenarios.	Does not support multi-streaming and multi-homing features.
[60], [114], [115], [116],	TinyOS	MAC Layer	TinyLPL	Allows sleep/wake-up implementation with user-defined intervals.  Uses short preambles for better energy efficiency.	May suffer from false alarms, including false positives and false negatives in the presence of external interference and the hidden terminal problem.
[117], [118], [119],			MultiMAC	Introduces the concept of a virtual gateway, which allows sensor network interoperability using heterogeneous MAC protocols.	Energy efficiency is more important than interoperability. LPL performs better in terms of energy utilization.
[120] [121], [122], [123], [124],		Network Layer	TinyRPL	Implements an IPv6 stack based on 6LoWPAN specifications.	Similar to ContikiRPL, TinyRPL uses OF0 and MRHOF for parent selection and routing construction, it is not a desired solution, considering diverse IoT applications and network scenarios.
[125], [127], [128], [129], [130], [131], [132]			QU-RPL	Achieves load-balancing in LLN networks. Provides a congestion detection mechanism for parent selection and better PRR.	The DIO overhead cost may result in overall delay in the large IoT network. Similarly, the mentioned parent selection procedure does not take node energy into consideration in switching from one parent to another. In the same way, the Tickletimer resetting strategy depends on network size. Needs a more efficient Tickletimer for better output. The last problem is that it might not be suitable for multimedia applications.
		Transport Layer	HDRTP, STCP, ERTP, RCRT	Unlike Contiki, TinyOS supports a variety of transport layer protocols, including HDRTP, STCP, ERTP and RCRT.	Needs to provide a proper congestion control mechanism. Must implement TFRC and DCCP.
[133], [134], [135], [137]	FreeRTOS	MAC Layer	FreeRTOS MAC	Offers three MAC implementations. CSMA-MAC: Provides a collision avoidance mechanism. TDMA-MAC: Useful to handle a large number of nodes. X-MAC: Provides low-power duty cycling.	CSMA MAC: May affect the routing operation.  TDMA MAC: Requires tight time synchronization and is very sensitive to underlying mobility and topology changes.  X-MAC: No proper collision avoidance is provided.
		Network Layer	6LoWPAN Nanostack	Provides an ICMP implementation along with NanoMesh, which covers multiple hops.	Does not provide an RPL implementation.
		Transport Layer	FreeRTOS TCP/IP and lwIP stack	FreeRTOS TCP/IP stack is based on uIP which simplifies the TCP and UDP operation for low-end IoT devices.  IwIP is based on IPv6 and 6LoWPAN to provide better energy management.	FreeRTOS TCP/IP is still under development, features like multi-streaming and multi-homing are not present yet.

layer [96].

#### a: Contiki support for MAC layer protocols

IoT resource management under a MAC protocol is usually achieved in terms of energy efficiency [97]. The MAC protocol approach developed for a duty-cycle IoT aims to reduce radio idle listening duration to minimize energy consumption. Idle listening is the time the node spends listening to the medium, even if no packet is present. The X-MAC protocol is implemented in the Contiki, and it provides a low-power listening mechanism [98]. If a node sends data, it transmits a preamble. The receiver wakes up, detects the preamble, and stays in the idle state to receive the data. In this basic approach, the receiver stays in the wake-up state until the preamble is finished, and it then starts the data- and acknowledge (ACK)-packet exchanges (Figure 5). The receiver may have woken up at the start of the preamble. This results in wasted energy. X-MAC replaces the low preamble with short strobe frames [99]. The receiver receives one strobe and transmits a strobe-ACK. The sender then proceeds with data transmission. Hence, a short preamble further decreases the time and energy consumption. However, X-MAC wakes up each node for a short active period in this procedure. The node goes to sleep mode again after an active period, which is 5% to 10% of the wake-up interval.

Contiki 2.4 introduced a carrier sense multiple access (CSMA) MAC protocol that simply detects a collision and retransmits the packets. However, this retransmission information is not passed to the upper layers in order to save computational costs. Hiding this information may affect the overall routing operation. Therefore, a new power-saving mechanism called the ContikiMAC radio duty cycling protocol was introduced in Contiki 2.5 [100]. The ContikiMAC radio duty cycle mechanism was inspired by the X-MAC duty cycling procedure [101].

ContikiMAC periodically wakes up the radio to listen for a packet transmission. The sending node continuously sends the data frame to the receiver until it gets an acknowledgment. The packet's destination field reduces overhearing, i.e., the node can go into sleep mode if it is not the packet destination. The receiver wakes its radio to listen for packet transmission. After detecting the packets, the receiver stays awake to receive the full transmission. Once reception of packets is done, it sends a link layer acknowledgment. This mechanism is illustrated in Figure 6.

The wake-up duration timing needs to be precise. To provide power-efficient wake-up timing, Contiki uses a mechanism called clear channel assignment (CCA), which utilizes the received signal strength indicator (RSSI) value to predict channel availability. An RSSI value lower than a given threshold returns "CCA positive," indicating the channel is free. Similarly, an RSSI value greater than the threshold amount returns "CCA negative," indicating the channel is busy. ContikiMAC follows precise timing constraints. ContikiMAC timing is illustrated in Figure 7;  $t_i$  is the time duration between two data packet transmissions,

which must be greater than the time required to transmit and receive the ACK, i.e.,  $t_a + t_d$ . The interval  $t_c$  between two CCAs ( $t_r$ ) must be greater than  $t_i$  to ensure two CCAs detect a frame. ContikiMAC uses a phaselock mechanism introduced by WiseMAC [102]. In this mechanism, the transmitter can estimate the wake-up schedule of the receiver with the ACK packet and can transmit data frames repeatedly just before the receiver is expected to be in the wake-up state. This phaselock mechanism in ContikiMAC reduces both energy and channel utilization, but at the risk of collision.

Some other MAC layer protocols were designed and tested with the Contiki OS. RAWMAC, a cross-layer approach is implemented in Contiki [103]. It exploits the Contiki RPL [104] protocol at the routing layer, and ContikiMAC at the MAC layer. It uses RPL's directed acyclic graph (DAG) and aligns node wake-up internally estimated by the ContikiMAC phase lock mechanism with its parent node to minimize data collection delay. Another MAC protocol implemented in Contiki is called GinLITE [105].

# b: Contiki support for network layer protocols

IETF provides IPv6 routing in low-power and lossy networks. RPL specifies how to construct a destinationoriented directed acyclic graph (DODAG). Each node is given a rank based on an objective function (OF). The rank provides the position of the node in the network. The OF calculates the rank of the node using a path calculation in a low-power and lossy network (RFC 6551) [106]. The node joining the RPL network first listens to a DODAG information object (DIO) message. If a node is unable to receive the DIO message, it will broadcast a DODAG information solicitation (DIS) message, which compels the neighboring node to broadcast the DIO message. Using the DIO message, the OF selects the parent node. The packet is forwarded to each parent node until the packet reaches the sink. When traffic is required to flow in the opposite direction, the routing state at every node is built using a DODAG destination advertisement object (DAO) message. The node sends a DAO message to its parent node, which will forward it through the parent's parent node to the sink [107]. Tsiftes et al. proposed a mechanism to implement RPL protocols inside uIPv6 [104]. Similarly, Ko et al. [108] tested the ContikiRPL implementation using two OFs, i.e., OF0 and a minimum rank objective function with hysteresis (MRHOF). ContikiRPL separates the OF into various modules. First, the protocol logic module maintains DODAG information and the node's parent-associated information. Second, the message-construction and parsing module provides the RPL ICMPv6 message format and data structure to ContikiRPL. Third, the OF modules provide an OF API. ContikiRPL provides a forwarding table mechanism for uIPV6 instead of taking a forwarding packet decision per packet. The link cost is estimated by a neighbor information module and is updated to the forwarding table. The uIPv6 layer forwards the outgoing packets to the 6LoWPAN layer, which provides header compression and fragmentation, and then, the packet is forwarded to ContikiMAC.

RPL faces congestion and packet loss problems during heavy traffic. Similarly, RPL has a fixed traffic configuration, it cannot adapt to IoT applications' varying traffic patterns. Mobility is another crucial issue that causes link breakage and invalid routes in DAGs. To address these problems, Tahir et al. proposed an extension of RPL called backpressure RPL [109], which combines the RPL OF with backpressure routing. Congestion issues in the 6LowPAN layer are evaluated in Contiki using a non-cooperative game theory mechanism [110].

Some other routing protocols have been implemented in the Contiki, e.g, the Mesh\_under Cluster\_based Routing (MUCBR) protocol proposed by Al-Nidawi et al. [111] reduces the node energy consumption and radio duty cycle by implementing a clustering structure under the 802.15.4 standard. Similarly, another mechanism is proposed to provide an improved RPL routing metric [112]. It combines a node residual energy ratio (RER) and a battery discharge index (BDI) along with expected transmission count (ETX) for parent selection and rank computation.

#### c: Contiki support for transport layer protocols

A traditional TCP/IP cannot be implemented in limited-resources devices; uIP provides the minimum features needed to implement the full TCP/IP stack [113]. It contains simple TCP and UDP transport layer protocols. However, UDP in uIP does not support broadcast or multicast transmission. Similarly, UDP checksums are also not provided in uIP.

# 2) TINYOS SUPPORT FOR COMMUNICATION PROTOCOLS

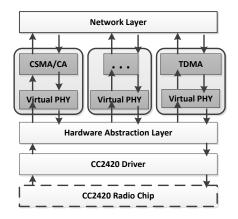
The basic communications paradigm of TinyOS is the active message (AM), a single-hop protocol [114]. AM is a simple networking primitive where each message includes an identifier to be invoked on the target node to pass the AM to its handler. In this way, this event-based communication between nodes provides a TinyOS publish/subscribe-based communications architecture. The GenericComm component in TinyOS 1.x provides AM communications interfaces, which provide single-hop unicast, and broadcast communications. TinyOS supports multi-hop communications protocols dissemination and TYMO (which is an implementation of the Dynamic Manet on Demand Routing Protocol (DYMO)) [115], [116]. Dissemination protocols are designed to reduce temporary disconnections and packet losses by ensuring the reliable delivery of data to every node in the network.

# a: TinyOS support for MAC layer protocols

Various MAC protocols have been tested with TinyOS. For example, B-MAC provides a low-power operation interface [117]. It introduced an adaptive preamble sampling

mechanism, which reduces radio duty cycling and idle listening. Similarly, X-MAC is a low-power listening approach that uses a short preamble to conserve energy [118]. The receiving node's address information is embedded in the preamble to resolve the overhearing problem, which in turn, saves energy for the non-receiving node. It also uses the idea of a short strobed preamble, which is created by adding pauses in the short preamble. The strobe preamble enables the receiving node to interrupt a preamble as soon as it wakes up and immediately recognizes its own address. Then, it transmits an ACK in the next pause after the preamble. In this way, instead of waiting for the entire preamble to complete, a node can start receiving packets without wasting time and energy. Similarly, a transmitter also does not need to send the remaining short preambles.

Similarly, TinyOS provides a MAC mechanism called TinyOS LPL, which is similar to the ConikiMAC technique [60]. TinyOS LPL allows the radio to implement sleep/wake cycles at user-defined intervals. The LPL receiver energy-saving mechanism saves energy by performing short, periodic receive checks. A node wakes up during every LPL period to sense the channel. If there is activity on the channel, the receiver node will switch its radio on to receive packets and send an ACK. The transmitter stops packet transmission upon receiving an ACK. The transmitter sends a packet only during the receive check interval of the receiver. In a traditional LPL mechanism, the sending node transmits a very long preamble to span a complete receive check period. The receiver node radio stays awake during the full duration of the sending node preamble, and waits for the data packets after that. Staying awake for a long period and reading bits consumes lots of energy. TinyOS LPL improved this mechanism by replacing the long preamble with a smaller packet transmission. Along with that, a low-power interface was introduced, which allows the user to deploy nodes with a pre-defined duty cycle percentage or sleep time. The TinyOS LPL mechanism provides energy efficiency through a radio duty-cycling mechanism. However, the



 $\label{eq:FIGURE 8.} \textbf{FIGURE 8.} \ \textbf{The architecture of MultiMAC network stack (adapted from [119])}.$ 

default inter-packet spacing (IPS) in TinyOS LPL is 8 ms, which may result in lower throughput. With the higher IPS (8 ms) the average packet reception ratio (PRR) is 11.67%.

A wide variety of MAC protocols were designed in order to provide energy efficiency to a sensor network. Due to compatibility problems, the sensor networks are not interoperable. To overcome this problem; a MultiMAC network stack to run multiple MAC protocols using a single radio interface was introduced [119]. The MultiMAC protocol stack utilizes three known protocols, CSMA with collision avoidance (CSMA/CA), LPL MAC, and TDMA MAC, on top of the same radio driver. It introduces the concept of using a virtual gateway to enable sensor network interoperability using heterogeneous MAC protocols. The MultiMAC network stack architecture is depicted in Figure 8

The CC2420 driver manages the data transmission, reception, and frame transmission timing. Similarly, the hardware abstraction layer (HAL) provides multiplexing of different MAC protocols. The HAL is responsible for dispatching the received frame to the correct MAC protocols using a MAC-id in the frame, performs address recognition, and sends automatic ACK packets for each MAC. Each MAC protocol is provided a virtual physical layer address to isolate them from one another.

# b: TinyOS support for network layer protocols

ContikiRPL, TinyRPL is based on the IETF RPL (RFC 6550) [13]. TinyOS 2.x utilizes an interface provided by the Berkeley low-power IP stack (blip), which implements an IPv6 stack based on 6LoWPAN specifications [120]; blip 6LoWPAN header compression, neighbor discovery, and DHCPv6 to provide IPv6 in the upper layer. The blip architecture is shown in Figure 9. The IP forwarding abstraction allows RPL implementation on top of ICMv6. As explained in ContikiRPL, the packet is forwarded once DODAG is constructed using a DIO message. Then, TinyRPL saves its route in the blip forwarding module. Both ContikiRPL and TinyRPL implement OF0 and MRHOF objective functions for route selection. However, the routing in RPL depends on other layer functions, as well. For example, the MAC layer retransmission timeout affects the RPL link discovery function. It is been shown that the ContikiRPL

retransmission timeout is two times the TinyRPL retransmission timeout because of the MAC layer retransmission timeout difference between them [108].

Load balancing under heavy traffic in RPL is carried out efficiently by queue utilization (QU) [121]. The proposed QU- RPL mechanism aims to achieve load balancing by offering a better parent-selection method, which results in less congestion at both the link level and the queue level, along with a better packet reception ratio. During DODAG construction, the DIO message contains information about the RPL node rank and routing metrics. The QU-RPL procedure added new QU information in DIO. During heavy traffic and fast Tickletime, the amount of overhead can cause severe delay in a large IoT network. Similarly, the proposed algorithm is required to add the battery's remaining energy information during parent selection. In the same way, the Tickletimer resetting strategy depends on network size. It requires a more efficient Tickletimer strategy for better output. Providing multimedia application support in this scenario is also very challenging due to constrained environment and overhead costs.

TinyOS Opportunistic Routing Protocol (TORP) for WSNs is designed to conserve energy by implementing an efficient forwarding mechanism [122]. Similarly, the Low-Energy Adaptation Clustering Hierarchy (LEACH) protocol was tested with TinyOS [123]. LEACH forms node clusters using their RSSI values. Then, the local cluster head (CH) is declared a router to communicate with the base station. In this protocol, energy conservation is based on the idea of preventing long distance communications by each node, since only the CH is responsible for communicating with the base station. Another energy-efficient routing protocol named Beacon Vector Routing (BVR) is implemented in TinyOS [124]. It defines a beacon vector routing metric and routes the packets in a greedy manner to the next closest hop, which is calculated by a beacon vector distance metric. proactive distance-vector protocol (Babel) implemented in TinyOS to support low-power sensor operation [125]. Babel is based on Destination-Sequenced Distance-Vector (DSDV) routing and Ad Hoc On-Demand Distance Vector (AODV) protocols. Location-aided routing (LAR), Destination-Sequenced Vector Routing (DSVR) and an event-driven data-centric routing protocol are also implemented in TinyOS to support sensors' battery lifetime

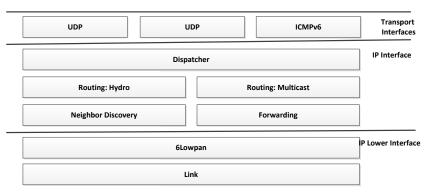


FIGURE 9. The basic architecture of the blip stack.

[126].

#### c: TinyOS support for transport layer protocols

IoT applications demand a certain level in quality of service plus specific resource requirements. TinyOS does not provide any specific transport laver protocol implementation. The blip interface in TinyOS provides a UDP socket layer as a basic transport layer implementation [120]. The UDPShell provided by blip contains simple commands, including help, echo, uptime, ping, and ident, to provide debugging commands to the sensor node. Similarly, blip also offers a very simple TCP stack. In order not to exhaust the sensor resources, the TinyOS implementation does not do receive-side buffering. The new packets are dispatched does not do transmitter-side buffering; the dropped segment is automatically retransmitted instantly [127].

Besides the above-mentioned transport layer support, TinyOS supports a variety of transport layer protocols to conserve energy. For example, Sensor Transmission Control Protocol (STCP) was implemented and tested in TinyOS [128]. In STCP, most of the functionalities are implemented at the base station. Thus, a considerable amount of energy is conserved in the sensor nodes. The sensor nodes associate themselves with the base station using a session initiation packet, which informs the base station about the number of flows, data types, and transmission types, and reliability requirements. Likewise, the Hybrid and Dynamic Reliable Transport Protocol (HDRTP) was proposed using TinyOS [129]. It has been shown that HDRTP enhances sensor node performance in terms of success rate, average latency, and average delivery ratio. Similarly, various transport layer protocols were tested on TinyOS, e.g., a post-order-based protocol [130], the Energy-efficient and Reliable Transport Protocol (ERTP) [131] and the Rate-Controlled Reliable Transport Protocol (RCRT) were implemented and tested in TinyOS [132].

# 3) FREERTOS SUPPORT FOR COMMUNICATION PROTOCOLS

FreeRTOS utilizes third-party additional tools and applications to provide networking support, e.g., Real Time Engineers Ltd. provides the FreeRTOS+TCP configuration [133]. The FreeRTOS+TCP configuration allows Ethernet-based IPv4 protocol networking solutions. Similarly, a third-party networking port stack e.g., uIP and IwIP, is utilized in a constrained environment [134].

#### a: FreeRTOS support for transport layer protocols

FreeRTOS, being a minimalistic OS, does not provide a native MAC layer implementation. However, a third-party implementation is available. For example, IoT-LAB provided a FreeRTOS-based MAC layer implementation to provide better real-time support [135]. They implemented three MAC layers, including CSMA, TDMA, and X-MAC. This CSMA implementation provides a user-defined

number of transmitting requests before packet dropping. TDMA is useful to handle a large number of nodes operating on the same channel. X-MAC provides a low-power duty cycling MAC mechanism that is suited to low-traffic networks. Schoofs et al. provided an 802.15.4 MAC implementation on a FreeRTOS micro-kernel [136]. In this configuration, MAC is considered an application that is run by a FreeRTOS task as a FreeRTOS MAC task and is given the highest priority.

## b: FreeRTOS support for network layer protocols

Real Time Engineers Ltd. provides FreeRTOS+TCP, which is an open-source TCP/IP stack [133]. It is based on a Berkeley sockets interface supporting an Ethernet-based IPv4 stack that offers support for UDP and TCP, and lwIP is based on a TCP/IP protocol suite with low RAM usage. IwIP is suitable for devices with 10 kB RAM. Hence, it is suitable for IoT low-end devices. Most of the FreeRTOS demos make use of an old IwIP version. At the network level, it supports Internet Protocol, Internet Control Message Protocol (ICMP) and Internet Group Management Protocol (IGMP). IwIP utilizes basic IP functionalities, it sends, receives, and forwards packets, but does not handle fragmented IP packets with IP options. The OS does not use function calls and data structures directly in the code. In order to make IwIP more portable, an OS uses an emulation layer to provide the IwIP functions. The emulation layer provides a common interface between the kernel and the IwIP code. This interface provides services that include a timer used by TCP/IP. It processes synchronization (semaphores) and has a message processing mechanism that uses an abstraction called mailboxes. The uIP implemented in Contiki does not support all UDP and multicast features, i.e., uIP can send UDP multicast messages, but is unable to join multicast groups and receive multicast messages [113]. Unlike uIP, IwIP provides the necessary UDP and multicast components. FreeRTOS also supports ports of an embedded network stack called Nanostack [137], which was developed by Sensinode. It is based on a 6LowPAN implementation and decreases RAM usage by executing as a single task under FreeRTOS. Similarly, 6LowPAN compresses the IPv6 headers to make them useful for resource-constrained sensor devices.

# c: FreeRTOS support for transport layer protocols

FreeRTOS uses TCP and UDP as transport protocols. FreeRTOS+UDP is a socket-based fully thread—aware stack for FreeRTOS. It provides a Berkeley socket—like interface with compact code size that makes it useful for communication between limited-resource IoT devices. The FreeRTOS+TCP stack, on the other hand, provides a more reliable stream service. Therefore, TCP contains 50% of the total code size of the lwIP stack [134].

#### E. FILE MANAGEMENT

A typical IoT network consists of thousands of tiny devices that sense the environment and process raw information.

This information sometimes needs to be stored. In the past, a wireless sensor network was communication-centric and used to transfer sensed data to one or more sensor devices or a base station. However, in recent years, the presence of onboard flash storage in IoT hardware platforms has provided a storage capability to the sensor network. As a sensor node's memory is a scarce resource, an efficient file system is required, although not every IoT scenario requires a file system. Contiki provides a flash-based file system called Coffee, which gives support to flash-based sensor devices [138]. A typical IoT device contains a few kilobytes of RAM. The onboard flash-based storage provides more memory capabilities. The file system must support storage-centric sensor applications and networking components' storage needs. In other words, how to store and retrieve the data in an efficient manner is handled by the file system. The OS in this scenario is required to support the file system in order to satisfy the IoT resource requirements. TinyOS, on the other hand, uses a singlelevel file based on the assumption that a node runs a single application at a time [139]. Similarly, FreeRTOS+FAT is a DOS-compatible, open source file system for FreeRTOS Table VIII summarizes the overview of file management.

#### 1) CONTIKI FILE SYSTEM

Contiki File System (CFS) is a virtual file system to provide an interface to different file systems [138]. Building storage abstraction is a challenging task in a limited-resource environment (little code and a small RAM footprint) [141]. In this scenario, Contiki provides a base for building such an abstraction to support various resource-constrained devices. The two file systems that implement CFS with full functionalities are CFS-POSIX and Coffee. CFX-POSIX on the Contiki platform runs in native mode. Other file systems supported by CFS are also available, such as CFS-EEPROM, CFS-RAM, and CFS-XMEM, but these file systems are constrained to a single file only. Coffee, on the other hand, supports a file system for flash (EEPROM)-based sensor devices. Each file system uses the CFS API for reading, writing, and extracting files in a mechanism similar to the Portable Operating System Interface for Unix (POSIX) API. Coffee provides a programming interface to develop an independent storage abstraction. Coffee uses a small RAM footprint profile, and requires 5 kB ROM for the code and 0.5 kB RAM at run-time. Hence, it is suitable for resource-constrained devices. Coffee allows multiple files to coexist on the same onboard flash chip, and provides 92% of the achievable direct flash driver throughput. In order to provide better memory management, the concept of micrologs was introduced. A microlog, unlike the conventional log structure, allows configuring the logs of individual files, providing a tradeoff between space and speed. Flash memory may increase the chance of memory corruption if it erases the page every time. Coffee spreads the spectrum evenly to reduce the risk of corruption.

#### 2) TINYOS FILE SYSTEM

TinyOS supports a single-level file system. It assumes that a node runs a single file in any given time period [139]. Therefore, a single-level file system is sufficient (e.g. TinyOS 1.x uses a microfile system called Matchbox, which provides an interface to read, write, delete, and rename files [142]). Matchbox is designed to provide reliability and low-resource consumption. The Matchbox filing system is very simple; it stores files in an unstructured way and provides only sequential reads and append-only writes. Other third-party file system implementations are also present, e.g. TinyOS FAT 16 supports SD cards aimed at reducing the overall power consumption of sensor nodes. TinyOS 2.x is based on the NesC programming language, which provides an abstraction layer that separates hardware interfaces and provides a framework for developing a portable application [143]. The portable implementation of the FAT file system allows a node to store a large amount of data. Similarly, the Efficient Log-structured Flash (ELF) file system for microsensors is implemented in TinyOS [144]. The ELF log structure provides better memory efficiency, low-power operation, and tailored support for the common types of sensor files.

#### 3) FREERTOS FILE SYSTEM

FreeRTOS uses the Super Lean FAT File System (FreeRTOS+FAT SL). FreeRTOS+FAT

TABLE VIII. An overview of file management.

Relevant References	os	Key Concept	Advantages	Limitations
[138]	Contiki	Coffee File System (CFS): Coffee.	Supports a file system for flash.	The size of files must be reserve beforehand; it introduces delay in real- time applications if it requires a bigger size than the reserved size.
[139]	TinyOS	Single level file system.	Reduces overall power consumption.	It cannot deal with a large number of files simultaneously.
[140]	FreeRTOS	Super Lean FAT File System	Provides a low memory footprint.	FAT file system is not suitable for the IoT, particularly because it was not designed to be used in flash memory.

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(FAT12/FAT16/FAT32) is a DOS/Windows-compatible embedded file system with the main objective of minimizing both flash and RAM footprint (< 4 kB and < 1 kB, respectively).

# IV. OPEN RESEARCH ISSUES AND RECOMMENDATIONS

To develop a practical and efficient IoT OS, many research challenges need to be addressed. The OS should mainly focus on the severe resource shortages and requirements for diverse IoT applications. In this section, we first discuss the general IoT OS research directions, and we then pinpoint some specific research directions.

Small memory footprint: For general research directions, we first argue that more research should be put into the effort to utilize a small memory footprint while providing a developer-friendly API and adding sophisticated features, which may require adding a new programming language or extensions of existing ones. An IoT device contains only a few kilobytes of memory. Hence, the fundamental characteristics of an IoT OS are to reduce the code size and utilize the minimum memory in an efficient manner.

Energy efficiency: Another general research direction is to consider a practical energy-efficiency mechanism to prolong the IoT device battery lifetime by designing more efficient network protocols. Similarly, leveraging the hardware features in a smarter manner can lead to better energy efficiency.

Reliability of IoT devices: The reliability of IoT device operation is extremely crucial, especially if they are deployed in a remote location. To support IoT complex deployments, OS reliability can be achieved by using a microkernel, memory protection units, static code analysis, etc.

*Real-time support:* IoT devices require diverse applications; some of them provide real-time operation. These real-time sensings tend to be time-sensitive. Thus, a timely real-time operation guarantee is another challenge faced by the IoT OS.

Scheduling model: The IoT OS also faces some limitations during task executions that affect the processor and which can lead to extra burden and load on the processor. They can also affect the system's energy efficiency and real-time capabilities.

*Network buffer management:* The network buffer is the main component of the IoT operation. This area requires extensive research to efficiently allocate limited memory to the packets.

*Programming language*: Choosing a standard programming language, such as C or C++, or an OS-specific language like NesC, can affect performance, safety, and portability.

Programming model: The IoT environment could have diverse application needs; application development is highly affected by the programming model. Therefore, the limitation with the programming structure also needs to be addressed.

Hardware abstraction layer: Research to reduce the amount of overhead in designing the HAL could benefit the overall OS efficiency, especially in dense and lossy networks. On the other hand, it is very important that the HAL be well-designed and portable to different platforms.

Real-time OS issues: Managing a real-time OS is quite challenging. In a complex IoT system, a simple RTOS task may result in complex run-time behavior. Extensive research is required to provide proper task priorities and processor shared timing. FreeRTOS utilizes a multithreading approach where each process can be interrupted, and the higher priority task can be executed in any given time period, which can delay low priority-task execution time. Similarly, the dependency between tasks may block execution of certain tasks, which may also result in unnecessary delay. Priority inversion, timing properties, and task dependencies require further investigation for RTOS systems. Predicting real-time behavior is very difficult; tasks may execute slower than predicted; they may fail during execution, or can have unexpected delays. Therefore, implementing an RTOS in the IoT environment, especially for time-critical applications, might cause a problem. Hence, dealing with these RTOS challenges is an important research direction.

Coexistence: With the growing application scenarios of IoT networks in a limited frequency spectrum, coexistence technologies are an ongoing research problem for OS and radio designers. It is a diverse research area that requires an optimal design of the physical, MAC, and network layers. Achieving wireless coexistence can provide spectrum resource sharing, traffic off-loading, and optimal connectivity for diverse IoT services.

Besides the above-mentioned challenges; we also highlight some specific issues, which are yet to be fully addressed.

Contiki protothreads: Contiki provides multi-threading using protothreads, which are stackless and lightweight. Each process runs to completion, and does not allow interrupt handlers to post new events. If an IoT application requires priority for processes and threads, Contiki would not be an ideal OS choice. Hence, extensive research could be done to provide proper process synchronization in Contiki.

TinyOS scheduling: The TinyOS scheduling mechanism is not suitable for all application scenarios. For example, the application of encryption security task execution time is very long. If the execution time of some tasks is longer, compared to others, it can affect the baud rate. Similarly, if the local task frequency is high, the OS may lose the other tasks, which affects the overall IoT communications system. TinyOS also fails to handle execution of abnormal tasks, which can lead to a system crash.

FreeRTOS scheduling: FreeRTOS is a small real-time OS and provides a very basic scheduling procedure, i.e., highest priority first. Therefore, it does not offer the possibility of hard real-time scheduling. FreeRTOS is well suitable to a small embedded system that has limited,

predefined tasks. Research should be done on designing FreeRTOS to handle large IoT system tasks with more advanced scheduling mechanisms.

Memory protection in Contiki: Contiki supports a dynamic memory management mechanism. It does not provide a memory protection unit (MPU). Hence, this area still needs to be explored.

*X-MAC:* X-MAC uses a stream of strobes for broadcasting. The strobes do not contain a destination address, which forces each node to wake up for a specific time period. It also does not provide a collision avoidance mechanism, i.e., no clear channel assessment is provided.

ContikiMAC: ContikiMAC is an asynchronous radio duty-cycling procedure. The nodes perform clear channel assignment to determine the presence of ongoing transmissions. It is based on radio signal strength. If this signal strength threshold is not calibrated properly, ContikiMAC can face false and unnecessary wake-ups. This could be an interesting research area in order to provide proper CCA threshold calibration. Similarly, the phase-lock mechanism in ContikiMAC allows a node to learn neighboring nodes' wake up phases. More research efforts can be devoted to improving the phase-lock accuracy.

TinyOS LPL: TinyOS LPL provides low-power listening implementations to radios with predefined intervals. A basic problem with LPL is that it may suffer from false alarms, including false positives (waking up when there is no activity on the channel) and false negatives (falling asleep when there is traffic on the channel). LPL performance can degrade dramatically in the presence of external interference and hidden terminal collisions. An open research area is to study the complex relationships between TinyOS LPL duty cycling false positives, false negatives, and latency in the presence of external interference and the hidden terminal problem.

*ContikiRPL* and *TinyRPL* interoperability: Interoperability between different protocols is of utmost importance, especially in commercial IoT networks. ContikiRPL performs better in an independent implementation but can affect system performance in a mixed setup. Similarly, ContikiRPL interaction with different MAC protocols has not been investigated yet. Therefore, there is still a need to carry out extensive research to make the ContikiRPL protocol interoperable [145]. Similarly, the current RPL protocols do not support mobility. Hence, a lot of research issues need to be explored, for example, neighbor discovery, link quality estimation, and mobility pattern identification.

Network and link layer interoperability: The interoperability on the network and link layers is highly important. ETSI has developed plugtest events to solve this kind of problem. The shortcomings of the standards can be tested to develop a new standard or update an existing one.

Multimedia IoT device transport protocol support: Contiki and FreeRTOS provide a simple implementation of the TCP and UPD protocol stack. The IoT contains wide application scenarios for multimedia applications where the battery is either provided by solar or green energy. In these cases, using SCTP is more suitable, which provides continuous and event-driven data flow support. Hence, research is required to implement an efficient protocol at the transport layer in the Contiki and FreeRTOS to fit the requirements of multi-streaming and multi-homing features.

Congestion control mechanism: The IoT contains a huge number of devices, for example, IEEE 802.11ah access points can support approximately 8000 devices. With this huge number of devices, there is a need to provide an optimal congestion control mechanism. One possible solution is to implement TCP-friendly rate control (TFRC) and a datagram congestion control protocol (DCCP). Therefore, implementing a transport layer protocol with the congestion issue simplified by the TFRC or DCCP should be considered in future research directions.

Contiki file system: The Coffee file system provides an excellent storage abstraction in a limited-resource environment. The size of the files requires them to reserve sizes beforehand. If the size of the file is more than the reserved size, Coffee will make a new file with the bigger size. Thus, it will copy all the data from an old file to a new file. This introduces some delay in real-time application scenarios. Similarly, Coffee supports random access semantics, which adds some complexity in delay for log and storage read/write operations.

TinyOS file system: The TinyOS single-level file system provides easy access to files in the directory. However, the main limitation is that the system cannot deal with a large number of files simultaneously. It is also inconvenient to name a large number of files.

FreeRTOS file system: The FreeRTOS super lean FAT file system is a DOS-compatible basic embedded file system. This file system has yet to mature. It needs further research to enhance storage abstraction capabilities of an RTOS system.

*New OS challenges:* There are some new OSs, like RIOT [146], Mbed OS [147], embedded Linux, and Zephyr [148]. There is a need to study these OSs in detail to explore future research challenges.

#### V. CONCLUSION

In this paper, an effort is made to provide insight into various proposed approaches in the IoT OS resource management research area. This paper provides the characteristics of different IoT OS protocols, their design strategies, along with their relevant advantages and limitations. The contributions are multi-fold. First, the IoT concept, various standardization efforts, and motivations to study the management of IoT resources through an IoT OS are provided. Second, various previous surveyed papers are discussed. Third, each resource management aspect of Contiki, TinyOS and FreeRTOS is elucidated. Their resource management mechanisms are classified into various sections, including process management, memory management, communication management, energy

management, and file management. These approaches are further classified according to their problem formulations. Each OS's main approach is discussed with a summary of its underlying idea followed by its advantages and limitations. Finally, open research issues are split into two categories; i.e., general and specific directions for future research, with recommendations given. We believe this survey will stimulate the research community, and pave the way towards more-efficient and robust OSs for low-end devices.

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